



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hongyong Zhang
Serial No. : 09/362,808
Filed : July 28, 1999
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Art Unit : 2814
Examiner : Shrinivas H. Rao

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	43	-	40	=	3	\$150
Independent	6	-	6	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0
Applicant hereby petitions under 37 C.F.R. §1.136 for a 1 month extension of time.						\$120
Request for Continued Examination Fee						\$790
TOTAL FEE DUE						\$1,060

A check for \$1,060 is attached.

Respectfully submitted,

Date: 8/5/05


John F. Hayden
Reg. No. 37,640

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